Notice of Allowability  The MAILING DATE of this communication and	Application No.  10/628,912  Examiner	Applicant(s)  QUEK ET AL.  Art Unit	
	Examiner	Art Unit	
The MAILING DATE of this communication and			
The MAILING DATE of this communication and	Walter L. Lindsay, Jr.	2812	
All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT I of the Office or upon petition by the applicant. See 37 CFR 1.31	S (OR REMAINS) CLOSED in to 5) or other appropriate commur <b>RIGHTS.</b> This application is su	nication will be mailed in due cou	urse. <b>THIS</b>
1. This communication is responsive to an Election filed 8/2	<u>26/2004</u> .		
2. The allowed claim(s) is/are <u>1-10</u> .			
3. $oxed{oxed}$ The drawings filed on <u>28 July 2003</u> are accepted by the E	Examiner.		
<ul> <li>4. Acknowledgment is made of a claim for foreign priority of a) All b) Some* c) None of the:</li> <li>1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority of International Bureau (PCT Rule 17.2(a)).</li> <li>* Certified copies not received:</li> </ul>	ve been received. ve been received in Application	No	n from the
Applicant has THREE MONTHS FROM THE "MAILING DATE noted below. Failure to timely comply will result in ABANDON THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		a reply complying with the requir	ements
<ol> <li>A SUBSTITUTE OATH OR DECLARATION must be sub- INFORMAL PATENT APPLICATION (PTO-152) which gi</li> </ol>			ICE OF
6. CORRECTED DRAWINGS (as "replacement sheets") me	ust be submitted.		
(a) I including changes required by the Notice of Draftspe	erson's Patent Drawing Review	( PTO-948) attached	
1)  hereto or 2)  to Paper No./Mail Date	<u> </u>		
(b) including changes required by the attached Examine Paper No./Mail Date	er's Amendment / Comment or i	n the Office action of	
Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in			ck) of
7. DEPOSIT OF and/or INFORMATION about the department attached Examiner's comment regarding REQUIREMENT			e the
	•		
Attachment(s)  1. M. Notice of References Cited (PTO-892)	5   Notice of Info	ormal Patent Application (PTO-1	52)
<ol> <li>Notice of References Cited (PTO-892)</li> <li>Notice of Draftperson's Patent Drawing Review (PTO-948)</li> </ol>		, ,	<i>VL j</i>
	Paper No./N	fail Date	
<ol> <li>Information Disclosure Statements (PTO-1449 or PTO/SB Paper No./Mail Date <u>7/28/2003</u></li> </ol>	3/08), 7. ∐ Examiner's A	mendment/Comment	
4.   Examiner's Comment Regarding Requirement for Deposit	<u> </u>	Statement of Reasons for Allowa	nce
of Biological Material	9.  Other		

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## **DETAILED ACTION**

This Office Action is in response to an Election filed on Currently, claims 1-20 are pending.

## Election/Restrictions

- 1. Applicant's election of claims 1-10 in the reply filed on 8/30/2004 is acknowledged. Because applicant did not distinctly and specifically point out the supposed errors in the restriction requirement, the election has been treated as an election without traverse (MPEP § 818.03(a)).
- 2. Claims 11-20 are withdrawn from further consideration pursuant to 37 CFR 1.142(b), as being drawn to a nonelected device, there being no allowable generic or linking claim. Applicant timely traversed the restriction (election) requirement in the reply filed on 8/30/2004.
- 3. This application is in condition for allowance except for the presence of claims 11-20 to a device that is non-elected without traverse. Accordingly, claims 11-20 have been cancelled.

## Allowable Subject Matter

- 4. Claims 1-10 are allowed.
- 5. The following is an examiner's statement of reasons for allowance: the prior art, either singly or in combination fails to anticipate or render obvious, the limitations of:
- ...forming a thyristor having at least four layers with three P-N junctions therebetween, at least two of the layers being formed horizontally on the semiconductor

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substrate and at least two of the layers being formed vertically on the semiconductor substrate, as required by claim 1.

Lastly the prior art, either singly or in combination fails to anticipate or render obvious, the limitations of:

...depositing nitride spacers around at least one of the polysilicon gates and the P-Si pillar;

forming N+ implantations in the N- source/drain areas, in the top of the P-Si pillar, and in the top of the polysilicon gate over the P-implantation region;

forming a P+ implantation in the N-implantation adjacent the P-Si pillar, using the nitride spacer around the P-Si pillar to self-align the P+ implantation thereadjacent;

forming respective silicide layers over the N+ and P+ implantations; and forming an interconnect between the silicide layer over the top of the P-Si pillar and one of the silicide layers over the N- source/drain areas, as required by claim 6.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Walter L. Lindsay, Jr. whose telephone number is (571) 272-1674. The examiner can normally be reached on Monday-Thursday.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John F Niebling can be reached on (571) 272-1679. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

October 14 2004

John F. Niebling
Supervisory Patent Examiner
Technology Center 2800